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PTO/SB/81 (02-01)

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Application Number	10/079472
Filing Date	February 19, 2002
First Named Inventor	MAITREYEE MAHAJANI
Title	Gate Dielectric Structures for Integrated Circuits and Methods for Making and Using Such Gate Dielectric Structures
Group Art Unit	2814
Examiner Name	Thao X. Le
Attorney Docket Number	MA-068

I hereby appoint:

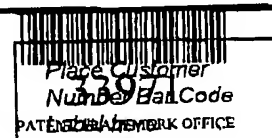
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Name	Registration Number
Liza K. Toth	31,065



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I am the:

☐ Applicant/Inventor.

☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

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SIGNATURE of Applicant or Assignee of Record

Name

LIZA K. TOTH VP, INTELLECTUAL PROPERTY

Signature

Liza K. Toth

Date

1/17/03

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☒ Total of 1 forms are submitted.

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Maitreyee MahajaniApplication No./Patent No.: 10/079472Filed/Issue Date: February 19, 2002Entitled: Gate Dielectric Structures for Integrated Circuits and Methods for Making and Using Such Gate Dielectric StructuresMATRIX SEMICONDUCTORa Corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

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1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
The extent (by, percentage) of its ownership interest is _____ %

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in the patent application/patent identified above by virtue of either:

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- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

1/17/03
Date

Liza K. Toth

Typed or printed name

Liza K. Toth
Signature

VP, Intellectual Property

Title

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